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Applicant(s): Hagios et al.

Docket No.
BUR920030063US1

Application No.
10/604,899

Filing Date
8/26/2003

Examiner
Tuyen T. Nguyen

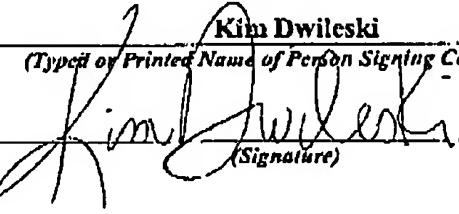
Group Art Unit
2832

Invention: WAFER TEST SPACE TRANSFORMER

I hereby certify that this Response to Restriction and Preliminary Amendment (5 pages)
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Kim Dwileski
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OCT 07 2004**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: Hagios et al.

Docket No.: BUR920030063US1

Serial No.: 10/604,899

Art Unit: 2832

Filed: August 26, 2003

Examiner: Tuyen T. Nguyen

Title: WAFER TEST SPACE TRANSFORMER

**RESPONSE TO RESTRICTION REQUIREMENT
AND PRELIMINARY AMENDMENT**

Commissioner for Patents & Trademarks

P. O. Box 1450

Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed on Sept. 7, 2004, Applicants hereby provisionally elects Group I, claims 1-9, drawn to a space transformer, classified in class 336, subclass 200. This election is made with traverse, and Applicants hereby reserve the right to file a divisional application in connection with unelected claims 10-21, drawn to a wafer test apparatus, classified in class 324, subclass 752.

In response to the Election Requirement mailed on Sept. 7, 2004, Applicant hereby provisionally elects the species of FIG. 13A-21B, the claims readable thereon being 1 through 9. Applicants believe claims 1-2 and 4-8 are generic.

With regard to the Restriction Requirement, Applicants respectfully submit that the subject matter of all claims 1-21 is sufficiently related that a thorough search for the subject matter of any one group of claims would encompass a search for the subject matter of the remaining claims. Thus, Applicants respectfully submit that the search and the examination of the entire application could be made without serious burden. See MPEP § 803, in which it is stated that "if the search and examination of the entire application can be made without serious

burden, the Examiner must examine it on the merits, even though it includes claims to independent or distinct inventions" (emphasis added). Applicants respectfully submit that this policy should apply in the present application in order to avoid unnecessary delay and expense to Applicants and duplicative examination by the Patent Office.